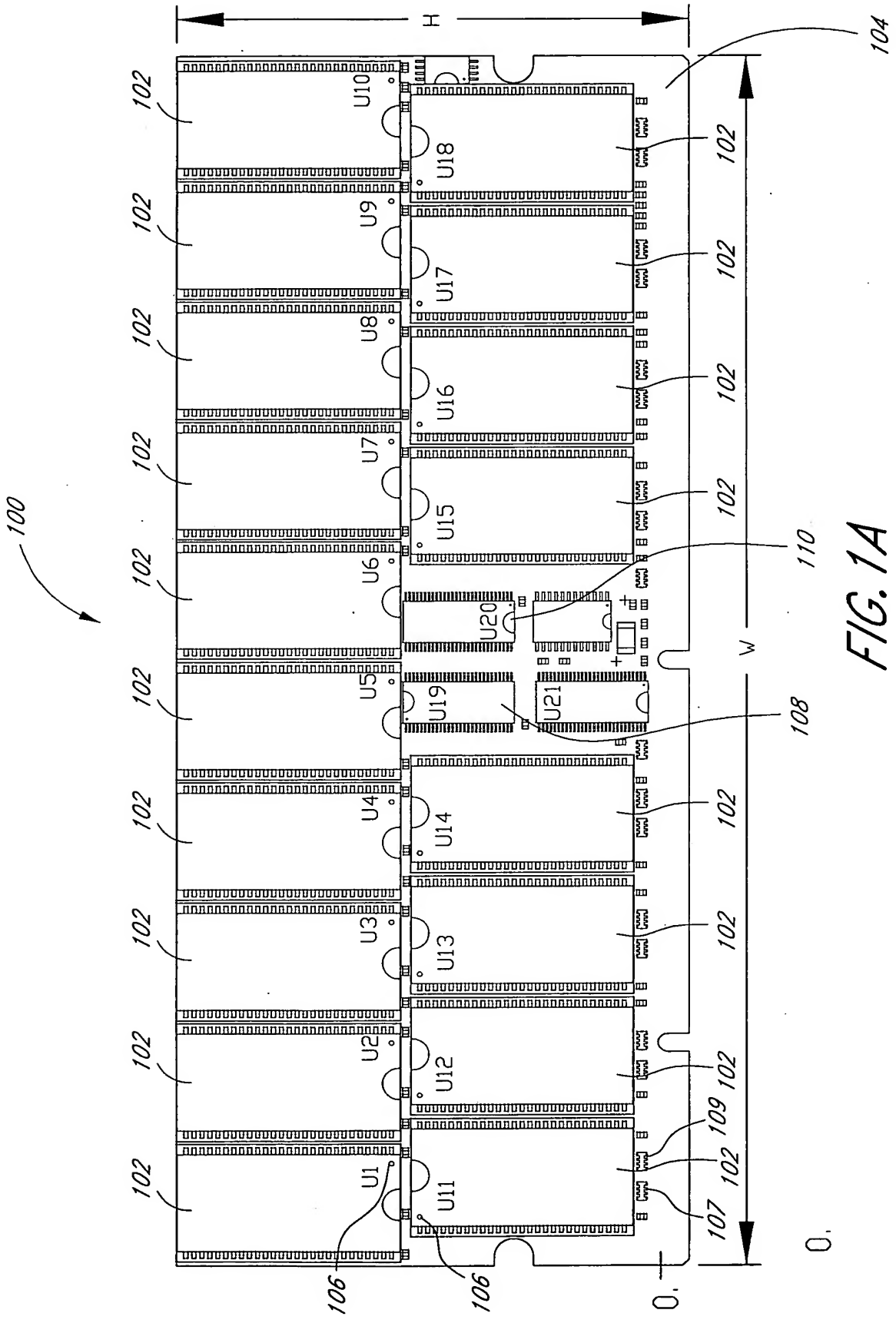
100
H
W
102
104
106
107
108
109
110
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U19
U20
U21

FIG. 1A

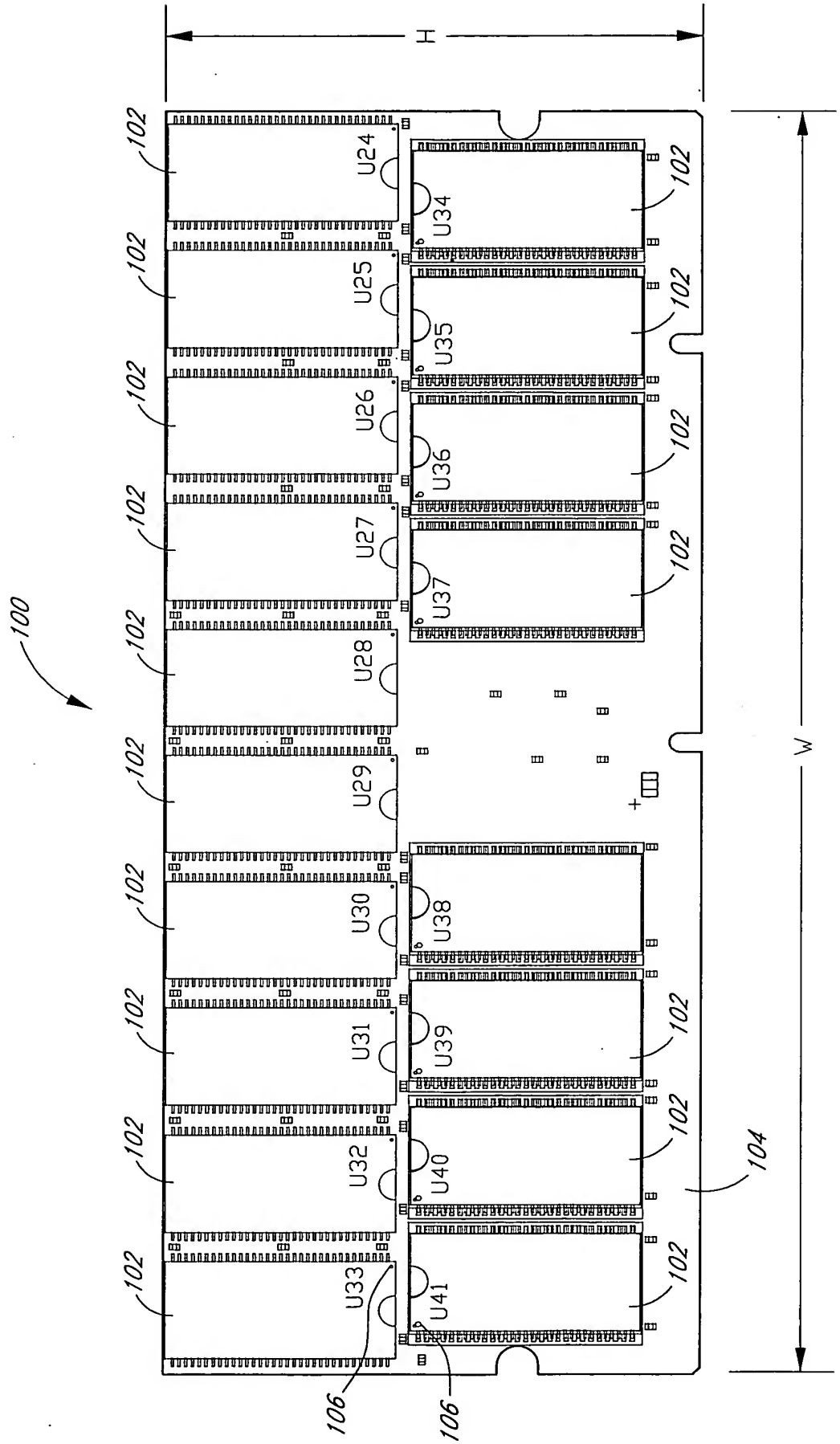
H
W
100
102
104
106
U24
U25
U26
U27
U28
U29
U30
U31
U32
U33
U34
U35
U36
U37
U38
U39
U40
U41

FIG. 1B

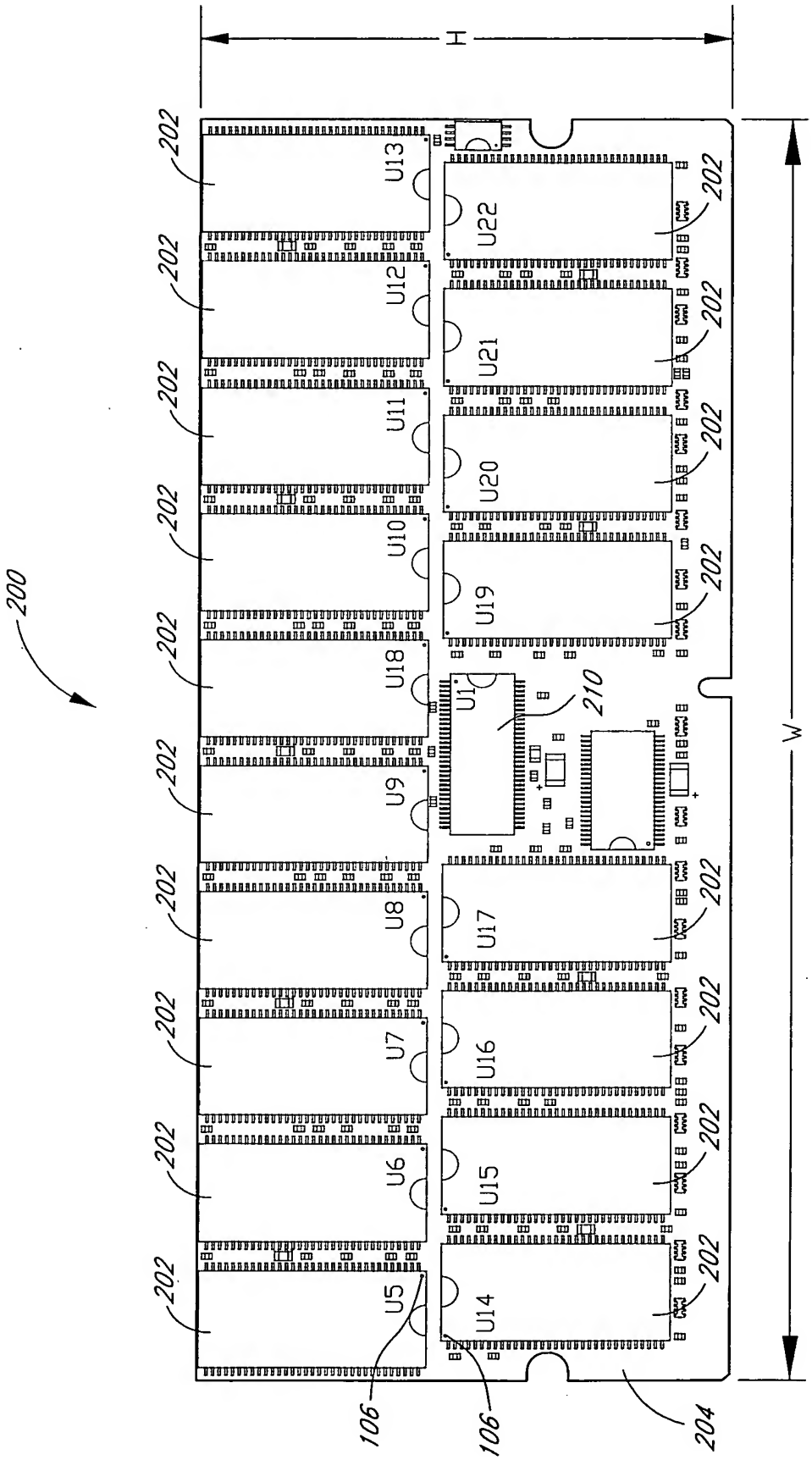
H
W
200
202
U1
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U19
U20
U21
U22
210
106
106
204

FIG. 2A

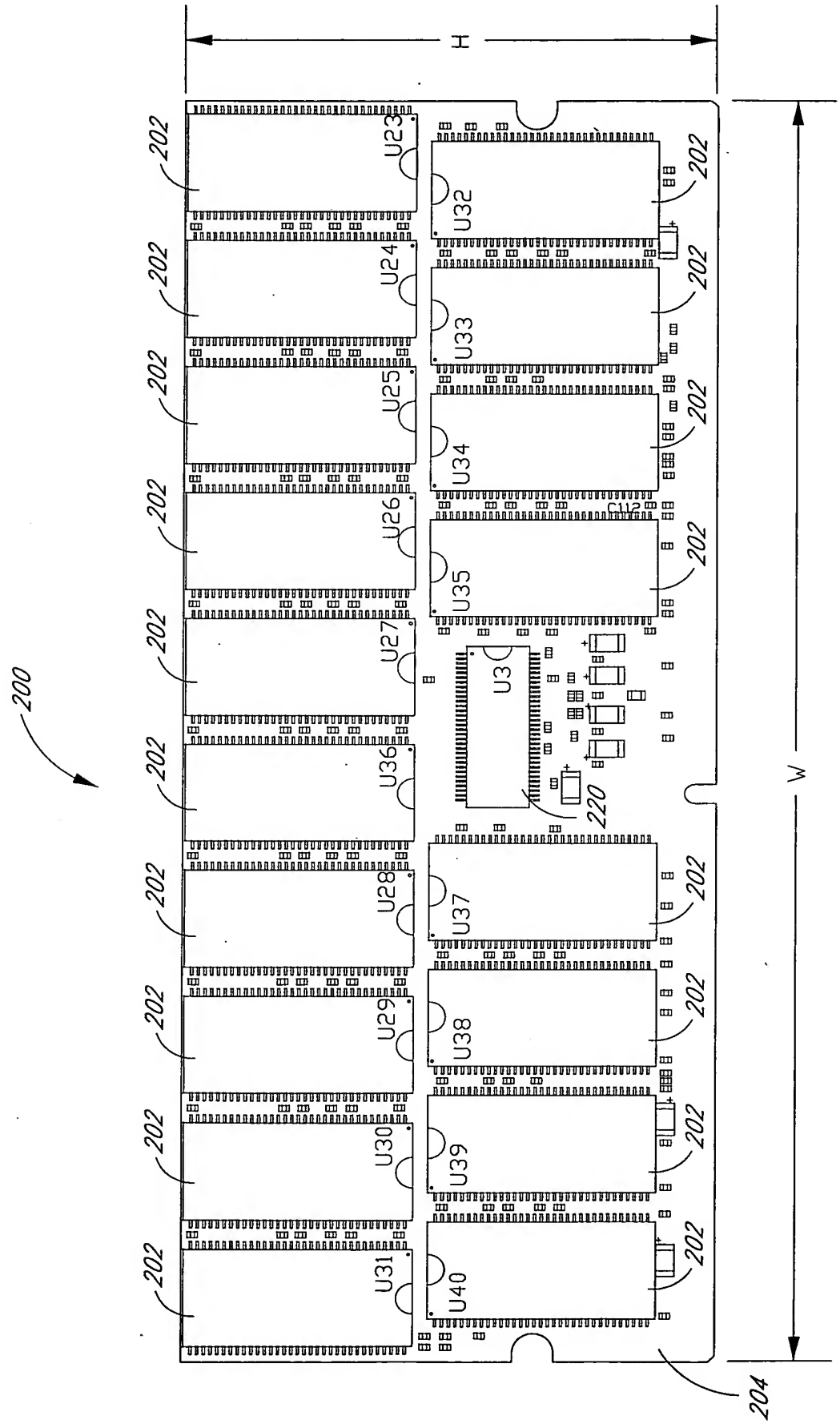
H
W
200
202
204
220
U23
U24
U25
U26
U27
U28
U29
U30
U31
U32
U33
U34
U35
U36
U37
U38
U39
U40
U3
C112

FIG. 2B

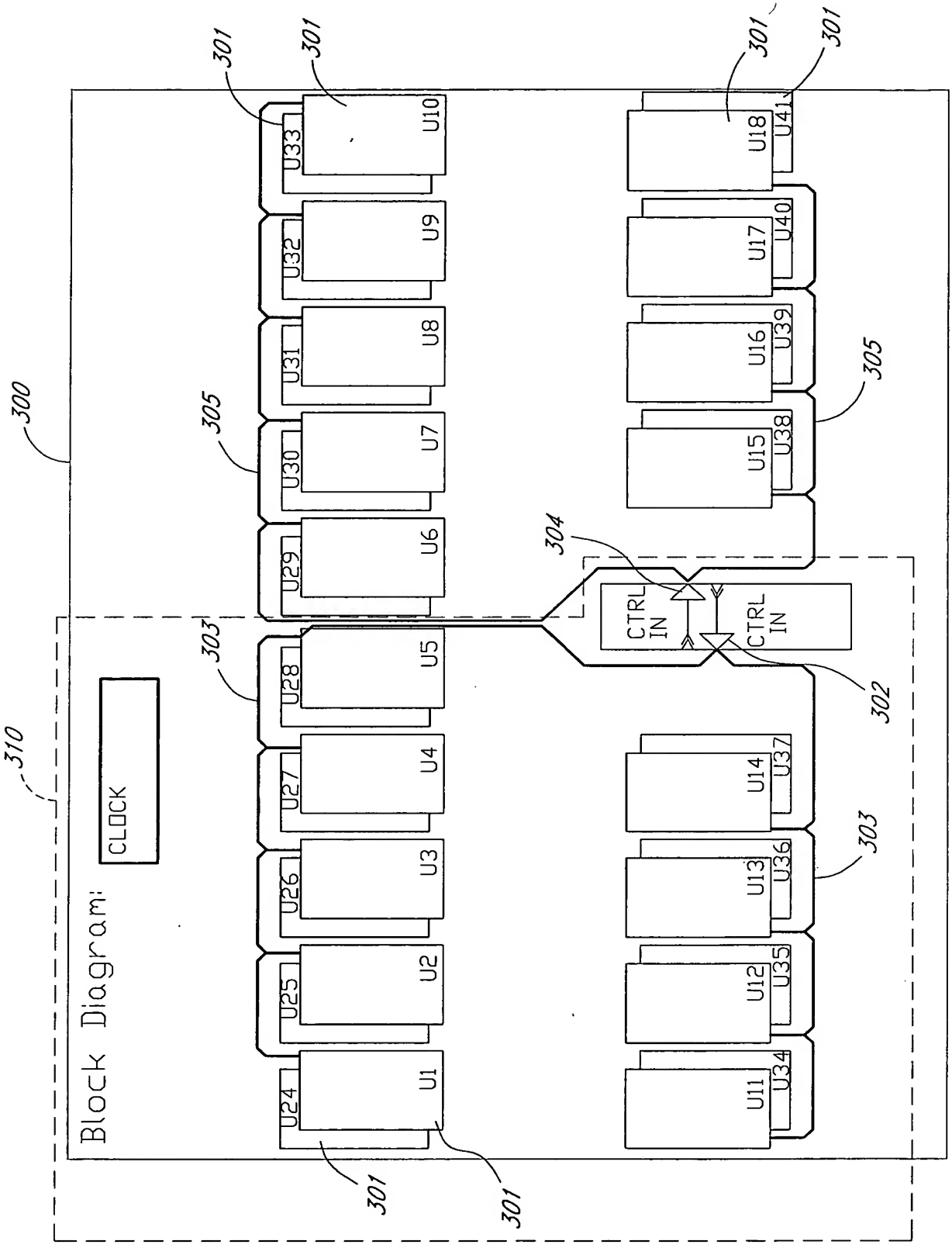
300
301
303
304
305
310
302
Block Diagram:
CLOCK
CTRL IN
CTRL IN
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U24
U25
U26
U27
U28
U29
U30
U31
U32
U33
U34
U35
U36
U37
U38
U39
U40
U41

FIG. 3A

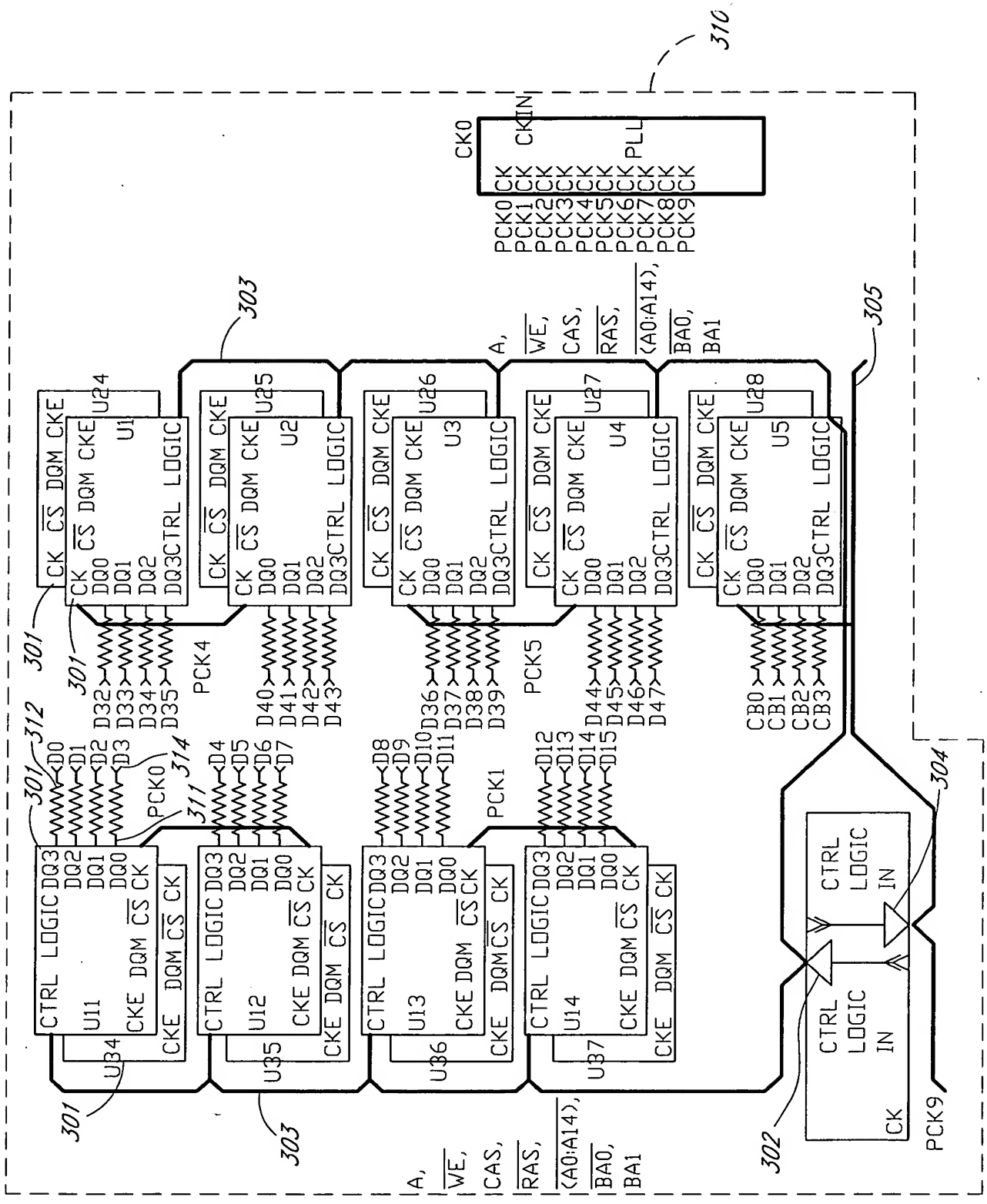
316
CK0
CKIN
PLL
PCK0
PCK1
PCK2
PCK3
PCK4
PCK5
PCK6
PCK7
PCK8
PCK9
303
305
A,
WE,
CAS,
RAS,
(A0:A14),
BA0,
BA1
U24
U25
U26
U27
U28
U1
U2
U3
U4
U5
CK CS DQM CKE
DQ0
DQ1
DQ2
DQ3
CTRL LOGIC
301
312
314
311
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
D32
D33
D34
D35
D40
D41
D42
D43
D36
D37
D38
D39
D44
D45
D46
D47
CB0
CB1
CB2
CB3
U11
U12
U13
U14
U34
U35
U36
U37
CKE DQM CS CK
304
CTRL LOGIC IN
302

FIG. 3B

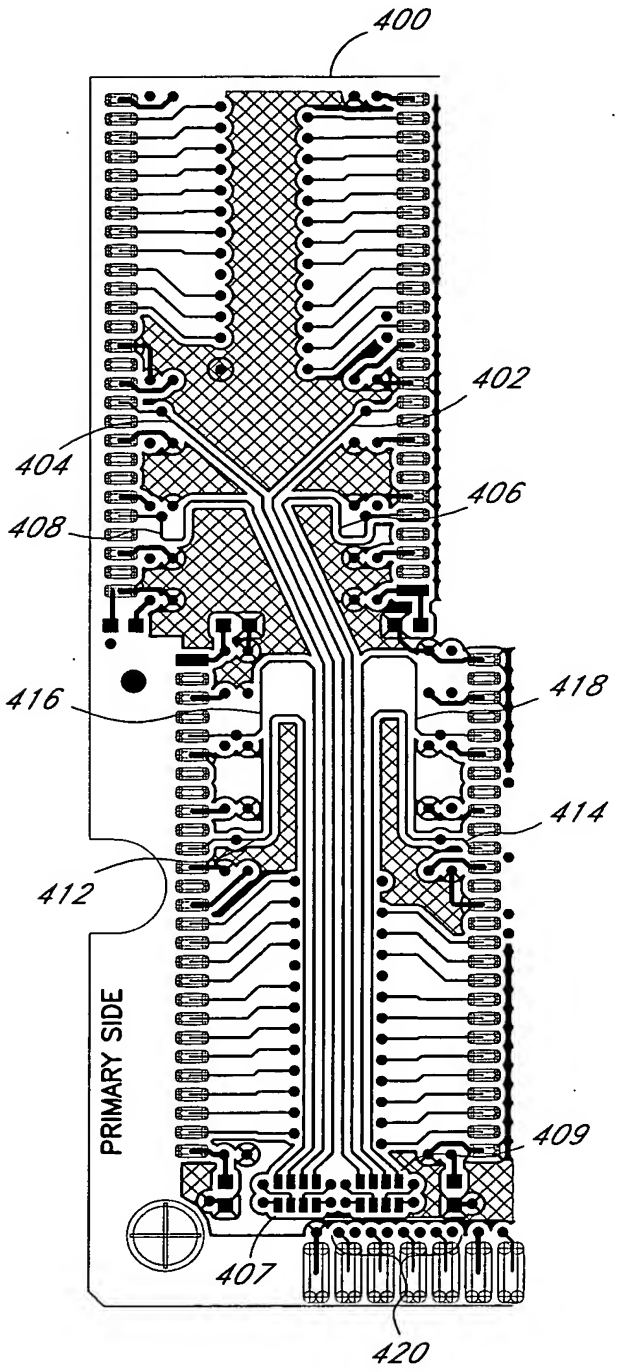

FIG. 4A

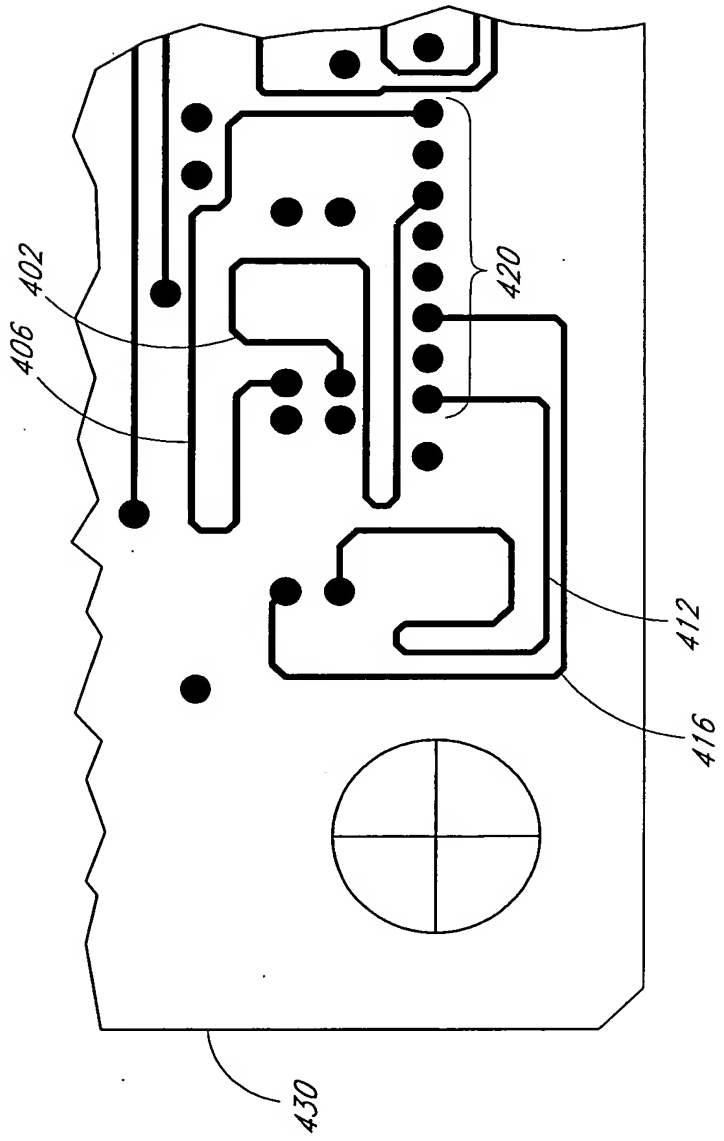

FIG. 4B

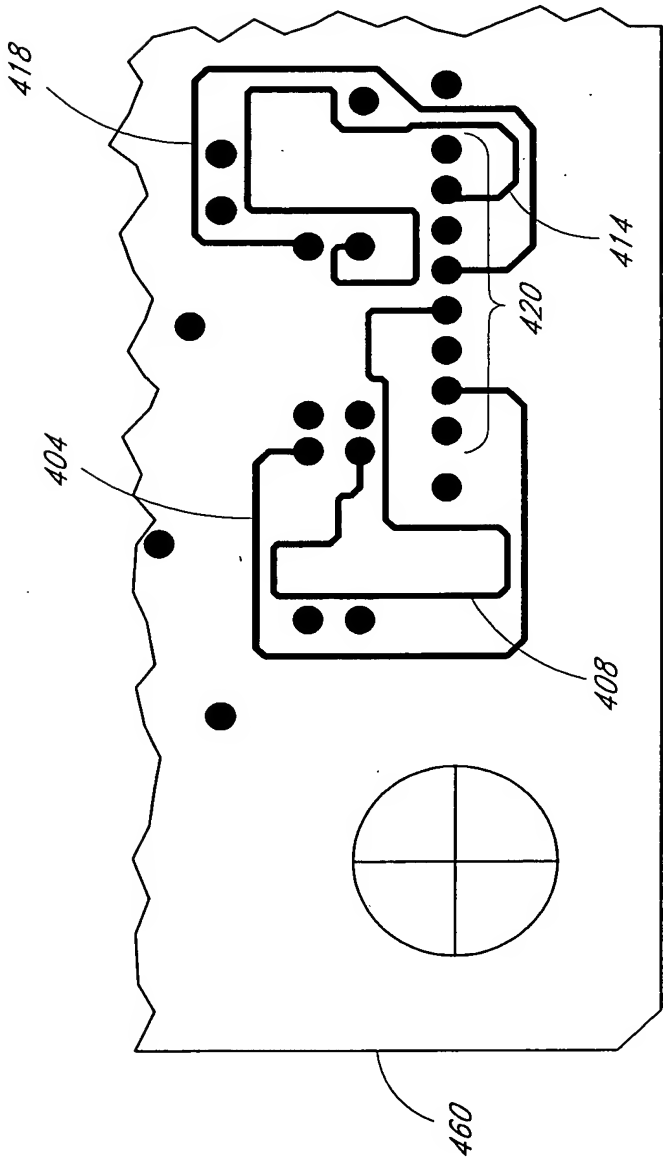

FIG. 4C